IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Kamiko, *et al.*	Docket No.:	2004 LW 2488 US
Filed:	Herewith	Examiner:	TBD
Serial No.:	Not Assigned	Art Unit:	TBD
Title:	Methods for Operating a CPU Having an Internal Data Cache		

Mail Stop PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

Prior to examination on the merits, Applicant respectfully submits this Preliminary Amendment and remarks as set forth below: